

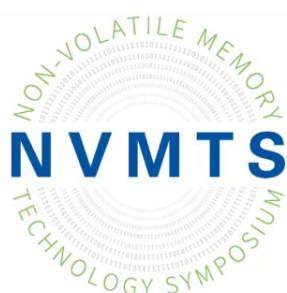
NVMTS 2024

The 22nd IEEE Non-Volatile Memory Technology Symposium

OCTOBER 21-23, PARADISE HOTEL BUSAN (HAEUNDAE BEACH), BUSAN, KOREA



www.nvmts2024.org



Fascinating discoveries and technological advancements in recent years have paved the way for new and competitive non-volatile memory (NVM) designs. At NVMTS 2024, invited representatives from all branches of memory technology in both industry and academia will present state-of-the-art research and provide critical insight into future challenges for both existing and emerging technology.

NVMTS is the eminent forum for exchanging information on non-volatile memory and we aim to create an interactive environment for discussing the most critical aspects of advanced memory technologies that will benefit everyone involved in this field, from graduate students to seasoned industry researchers.

The symposium will include a program with keynote speeches, invited talks, and contributed presentations to report the latest research in non-volatile memory. Please visit NVMTS website <http://www.nvmts2024.org/> for more information and instructions on how to submit the poster abstracts.

1. Flash
2. MRAM
3. PCRAM
4. RRAM/CBRAM
5. FeRAM
6. NVDRAM
7. Selector
8. Neuromorphic Materials and Devices

KEY DATES

Abstract Submission Deadline	Jun. 15, 2024
Notification of Acceptance	Jul. 4, 2024
Full Paper Submission Deadline	Nov. 20, 2024
Online Registration Deadline	Sep. 20, 2024

Conference Chair Hongsik Jeong (UNIST, Rep. of Korea)

Technical Chairs Ilya Karpov (Intel, USA)
Eilam Yalon (Technion, Israel)



NVMTS 2024 Secretariat

Tel.: +82-42-472-7460 | E-mail: secretariat@nvmts2024.org