# NVMTS 2024

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Bio: Datta is the Joseph M Pettit Chair of Advanced Computing and Georgia Research Alliance (GRA) Eminent Scholar and Professor in the School of Electrical & Computer Engineering at Georgia Tech. His research group focuses on semiconductor devices that accelerate current compute models as well as enable new compute models. From 2015 till 2022, Datta was the Stinson Endowed Chair Professor of Nanotechnology at the University of Notre Dame, where he was the Director of a multi-university microelectronics research center, ASCENT, funded by the Semiconductor Research Corporation (SRC) and the Defense Advanced Research Projects Agency (DARPA). Datta also served as the Director of a six-university research center for Extremely Energy Efficient Collective Electronics (EXCEL), funded by the SRC and National Science Foundation (NSF) that leverages continuous- time dynamics of emerging devices to execute optimization, learning, and inference tasks. From 1999 till 2007, he was in the Advanced Transistor Group at Intel Corporation, where he led device R&D effort for several generations of high-performance logic transistors such as high-k/metal gate, Tri-gate and strained channel CMOS transistors. He has published over 450 journal and refereed conference papers and holds more than 187 issued patents related to semiconductor devices. In 2013, Datta was named a Fellow of the Institute of Electrical and Electronics Engineers (IEEE) for his contributions to high-performance advanced silicon and compound semiconductor transistor technologies. In 2016, he was named Fellow of the National Academy of Inventors (NAI) in recognition of his inventions that have made a tangible impact on quality of life, economic development, and the welfare of society.