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**Dr. Masaru Toko**

(KIOXIA, JAPAN)



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Masaru Toko received the B.S. and M.S. degree in physics from Tokyo University, Japan, in 2007 and 2009, respectively. In 2009, he joined Toshiba Corporation as a researcher in the Research and Development Center. In 2019, He joined the Frontier Technology R&D Institute in KIOXIA Corporation. Since 2009, He has worked on MRAM development project and developed high performance MTJ material and structure for advanced MRAM devices. His current research is focused on energy efficient MRAM device designs, MTJ designs and technologies. He has 26 U.S. patents as 1<sup>st</sup> or co-inventor issued in MRAM device and improved MTJ property.