

NVMTS 2024

October 21-23, 2024 | Paradise Hotel Busan (Haeundae Beach), Busan, Korea

Shimeng Yu

(Georgia Institute of Technology, USA)



Shimeng Yu is a professor of electrical and computer engineering at Georgia Institute of Technology. He received the B.S. degree in microelectronics from Peking University in 2009, and the M.S. degree and Ph.D. degree in electrical engineering from Stanford University in 2011 and 2013, respectively. He is elevated for the IEEE Fellow for contributions to non-volatile memories and in-memory computing. His research interests include AI hardware and 3D integrated circuits. Among Prof. Yu's honors, he was a recipient of National Science Foundation (NSF) CAREER Award in 2016, IEEE Electron Devices Society (EDS) Early Career Award in 2017, ACM Special Interests Group on Design Automation (SIGDA) Outstanding New Faculty Award in 2018, Semiconductor Research Corporation (SRC) Inaugural Young Faculty Award in 2019, IEEE Circuits and Systems Society (CASS) Distinguished Lecturer in 2021, and IEEE Electron Devices Society (EDS) Distinguished Lecturer in 2022, and Intel Outstanding Researcher Award in 2023, etc. Prof. Yu's 400+ journal/conference publications have received more than 28,000 citations (Google Scholar) with H-index 81. Prof. Yu serves many premier conferences as technical program committee, including IEEE International Electron Devices Meeting (IEDM), IEEE Symposium on VLSI Technology and Circuits, IEEE International Reliability Physics Symposium (IRPS), ACM/IEEE Design Automation Conference (DAC), ACM/IEEE Design, Automation & Test in Europe (DATE), and ACM/IEEE International Conference on Computer-Aided-Design (ICCAD), etc. He serves an editor for IEEE Electron Device Letters (EDL), and associate editor-in-chief for IEEE Journal on Emerging and Selected Topics in Circuits and Systems (JETCAS).