

# NVMTS 2024

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**Prof. Wei Lu**

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**Wei D. Lu** is the James R. Mellor Professor in Engineering, and Professor in the Electrical Engineering and Computer Science department at the University of Michigan. He received B.S. in physics from Tsinghua University, Beijing, China, in 1996, and Ph.D. in physics from Rice University, Houston, TX in 2003. From 2003 to 2005, he was a postdoctoral research fellow at Harvard University, Cambridge, MA. He joined the faculty of the University of Michigan in 2005. His research interest includes resistive-random access memory (RRAM) devices, memristor-based logic circuits, neuromorphic and in-memory computing systems, aggressively scaled transistor devices, and electrical transport in low-dimensional systems. To date Prof. Lu has published over 200 journal and conference articles with 40,000 citations and H-factor of 90. He is a recipient of the NSF CAREER award, an IEEE Fellow, and co-founder of Crossbar Inc., which develops RRAM products, and MemryX Inc., which develops efficient edge AI chips.