

# NVMTS 2024

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## **Professor Xiaoxin Xu**

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Xu Xiaoxin is currently a professor at the Institute of Microelectronics of the Chinese Academy of Sciences. She received her master's degree from Shandong University in 2014 and her doctorate from the University of Chinese Academy of Sciences in 2017. She has since worked at the Chinese Academy of Sciences. She is mainly engaged in the research of high density integration and application of RRAM. She has published over 70 papers, including more than 10 papers at IEDM/VLSI. She is currently a member of the Technical Committee of the International Memory Workshop (IMW).