

# NVMTS 2024

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## **Dr. Matthew Hollander**

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Dr. Hollander is a Senior Engineer and Device Lead in DRAM Device and Cell Technology group at Micron, USA. His focus has been on device integration and optimization for emerging memories and for planar DRAM, specifically the overlap of device characteristics with memory performance and array integrity. Since Matthew joined Micron in 2015, he has worked on developing various new memory technologies including NVDRAM (ferroelectric). His specialty is device characterization, defect analysis, and device-array interaction. His background was in novel 2D materials for device applications with a focus on graphene and MoS<sub>2</sub>. Matthew received his undergraduate and graduate degrees from the Pennsylvania State University in Materials Science and Electrical Engineering.