

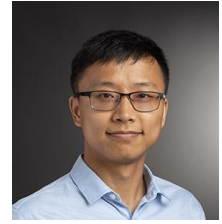
# NVMTS 2024

October 21-23, 2024 | Paradise Hotel Busan (Haeundae Beach), Busan, Korea

## Enabling High Density Data Storage and Computing in the Next Generation Solid State Drive with Ferroelectrics

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**Kai Ni** received the B.S. degree in Electrical Engineering from University of Science and Technology of China, Hefei, China in 2011, and Ph.D. degree of Electrical Engineering from Vanderbilt University, Nashville, TN, USA in 2016 by working on characterization, modeling, and reliability of III-V MOSFETs. Since then, he became a postdoctoral associate at University of Notre Dame, working on ferroelectric devices for nonvolatile memory and novel computing paradigms. He is now an assistant professor in University of Notre Dame since 2023 after joining Rochester Institute of Technology as an assistant professor. He has more than 100 publications in top journals and conference proceedings. His current interests lie in nanoelectronic devices empowering unconventional computing, domain-specific accelerator, and memory technology.