

NVMTS 2024

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Director & Professor Tetsuo Endoh

(Tohoku University, Japan)



Tetsuo Endoh graduated University of Tokyo, Faculty of Science in 1987 and received Ph. D degree from Tohoku University, Graduate School of Engineering in 1995. He joined ULSI Research Center Toshiba in 1987 and was engaged in the R&D and the mass-production of NAND Memory.

He became a lecturer at the Research Institute of Electrical Communication, Tohoku University in 1995. He is a professor at the Department of Electrical Engineering, the Graduate School of Engineering, Tohoku University and director of the Center for Innovative Integrated Electronic Systems (CIES). He became a research professor of Tohoku University in 2017. He became a research professor of Nippon Telegraph and Telephone (NTT) Corporation in 2023.

He received LSI IP Design Award of Nikkei Business Publications, 2001. He also received Paper Award of The Japan Society of Applied Physics, 2009 and Paper Award of the Japan Society of Vacuum and Surface Science, 2011. He received International Conference on Solid State Devices and Materials (SSDM) Paper Award, 2012. He received a 2020 VLSI Test of Time Award, VLSI Symposium, 2021.

Additionally, he received the 14th Prime Minister's Award, 2016. He received 2017 National Invention Award for his contribution of the invent of 3D-NAND Memory technology. He received Tohoku University President's Award in 2017. He was a Fellow of The Japan Society of Applied Physics from 2012 and a Fellow of the IEEE from 2023.